

Differential Audio Amplification System

Field of the Invention

- [0001] The present invention relates to multimedia computer speaker systems that play audio signals provided by host multimedia computers and, in particular, to such a system that is powered directly by such a host computer system.

Summary of the Invention

- [0002] Speaker systems with integral amplification electronics provide simple, compact audio playback for multimedia personal computers. These speaker systems, sometimes referred to as multimedia computer speaker systems, typically include pairs of wide-band speakers mounted in separate housings with amplification electronics incorporated into one or both housings. Because compactness is desirable, small, wide-band speakers (e.g., 3-inch diameter cone speaker drivers) are commonly used.
- [0003] Many conventional multimedia computer systems include two-channel (stereo) multimedia computer speaker systems. Typically, a conventional two-channel computer audio system includes a two-channel audio system control circuit, which is commonly configured as a separate computer expansion board called a "sound card," and two wide-band audio transducers or speakers. A sub-woofer audio transducer or speaker is also included in many implementations. Conventional two-channel speaker systems are configured to provide at the two wide-band speakers distinct audio playback according to two distinct audio channels included in a multimedia computer work such as a game, music, etc.

[0004] Computer systems and associated multimedia computer speaker systems are distributed throughout the world. Nearly all countries of the world have differing combinations of input voltage and frequency and regulatory approvals. Major computer system components, such as the central processing unit, usually have a universal input switch-mode power supply with universal regulatory approvals. These supplies have large power capacities (e.g., 200 to 400 watts) and easily absorb the additional electronics required for universal connectivity. In contrast, speaker systems are comparatively inexpensive and so the additional costs of universal supplies not affordable. As a consequence, the cost-optimized solution for speaker systems that are distributed world-wide is to use a unique power supply for each country of distribution. While being cost effective, this imposes extremely cumbersome product distribution complications for original equipment computer manufacturers, as well as other distributors.

[0005] One aspect of the present invention is a universal host-powered multimedia speaker system that derives its power from a host multimedia computer system chassis, rather than from a conventional dedicated speaker system power supply. This can be achieved by a custom power jack provided by the PC manufacturer for use by the speaker system, or use of power that is available on the pins of ports already available on the PC such as sound card game ports, the Universal Serial Bus (USB) ports, or the IEEE 1394 ports. The power is provided by the host computer system power supply, so the need for a separate external power supply or adapter is eliminated.

[0006] Another aspect of this invention addresses the audio performance consequences of the greater electrical noise in a conventional computer power supply in comparison to a dedicated speaker system power supply. In particular, a conventional computer power supply system has

09077639 060304
T03090 669/2860

much more electrical noise than a standard dedicated speaker system power supply or adapter.

[0007] Conventional computer system supply voltages include high-frequency electrical noise, which is caused by the varying current draws of hard drives, disk drives CPU chipsets, etc. While low frequency noise can be reduced by coupling most amplifier integrated circuits to relatively expensive large value electrolytic decoupling capacitors, such a configuration will still allow high frequency noise components to pass to the powered system because the high frequency components of the power signal are usually quite large and large value capacitors generally have poor high frequency rejection. In the case of multimedia computer speaker systems, such electrical noise can cause undesirable audible high-frequency noises to be generated by the powered speakers.

[0008] Additionally, other audible noise components can arise from differences between the electrical grounds for the audio electrical signal and the host power source for the speaker system. In particular, noise can be generated because of voltage differences between the two ground locations arising from the different currents flowing through the grounds and their finite impedances. A typical multimedia speaker system design cannot reject this ground difference electrical noise, and so will amplify the noise signal and play it as undesirable audio output.

[0009] Accordingly, another aspect of this invention is a multimedia computer speaker system having a pair of amplifiers with differential connections for receiving the audio signal and with differential output connections to the speaker to provide it with differential amplification. Differential input amplification amplifies the difference between the signals applied to the inputs of amplifiers and thereby rejects the common-mode noise, which includes electrical noise from differences

09877689-060001
1006035-6892860

between the electrical grounds for the audio electrical signal and the host power source.

[0010] Differential output amplification refers to a pair of amplifiers that generate output signals that are 180 degrees out of phase with each other. A load, such as a computer speaker, which is tied between and receives such signals is said to be driven differentially or a "bridge-tied load." This differential output amplification provides rejection of power supply electrical noise components. As a result, a differential input/differential output of the present invention allows a host computer-powered speaker system to provide low noise, high quality audio playback when used as a host powered speaker system.

[0011] Additional objects and advantages of the present invention will be apparent from the detailed description of the preferred embodiment thereof, which proceeds with reference to the accompanying drawings.

Brief Description of the Drawings

[0012] Fig. 1 is a simplified block diagram of a prior art three-piece multimedia computer speaker system coupled to a computer such as a personal computer.

[0013] Fig. 2 is a simplified block diagram of a universal host-powered multimedia speaker system that includes a differential input/differential output (DIDO) speaker driver system according to the present invention.

[0014] Fig. 3 is simplified block diagram of a differential input/differential output speaker driver system and an associated multimedia host computer system with representations of exemplary noise component sources.

[0015] Fig. 4 is a simplified block diagram of a differential stereo audio amplification system shown coupled to a stereo headphone.

[0016] Fig. 5 is a schematic diagram of a switch mechanism for switching a differential stereo audio amplification system between couplings to a multimedia speaker system and a headphone.

Detailed Description of Preferred Embodiments

[0017] Fig. 1 is a simplified block diagram of a prior art three-piece multimedia computer speaker system 10, which includes a pair of amplified wide-band speakers 12 and 14 and a sub-woofer 16. Speakers 12 and 14 and subwoofer 16 are commonly contained within separate housings with speakers 12 and 14 sometimes referred to as satellite speakers. Speaker system 10 receives an audio signal from an audio output circuit (e.g., a “sound card,” not shown) of a multimedia computer 20. Typically, the audio signal is a stereo audio signal that includes separate right and left audio channels that are applied to speakers 12 and 14, respectively. Speaker system 10 includes pair of equalizers 21 and 23 and a pair of amplifiers 22 and 24 that provide amplified right and left audio signals through high pass filters 25 and 27 to respective speakers 12 and 14.

[0018] Sub-woofer 16 receives an audio signal that typically is generated from the right and left audio channels. In particular, the right and left audio channels are passed to an adder or mixer 26 that sums the audio channels. The sum of the audio channels is passed through a low pass filter 28 to a sub-woofer amplifier 30 that is coupled to sub-woofer 16. As a result, subwoofer 16 is driven with a sum of the low-frequency components of the right and left audio channels. Speaker system 10 also commonly includes a volume control, such as a ground-connected potentiometer (not shown), that coupled somewhere between amplifiers 22, 24, and 30 and the sound card of host multimedia computer 20 to control the volume of audio playback provided by speaker system 10.

[0019] Conventional prior art speaker system 10 typically includes a dedicated power supply 32 that supplies power to each of amplifiers 22, 24, and 30 and the other electrical components of speaker system 10 (only the power connections to amplifiers 22, 24, and 30 are shown). Typically, dedicated power supply 32 is a DC power supply that is configured as a cubic block, sometimes called a "brick," with a rigidly integral plug that is inserted into a conventional AC power socket. Many other configurations of dedicated, stand-alone power supply 32 are known as well as integrated power supplies that may be included inside the enclosure of speaker system 10.

[0020] While simple and relatively convenient for a user, dedicated power supply 32 can pose significant product distribution and logistics difficulties if speaker system 10 is to be distributed or sold world-wide. The problem is that nearly every country has a different combination of input voltage and frequency and regulatory approvals. As a result, a unique power supply 32 with distinct input power and regulatory certifications is typically required for each country. This can add significant logistical complication and expense to the distribution of speaker systems 10 or other products, such as multimedia computers, that might be distributed with speaker systems 10.

[0021] Fig. 2 is a simplified block diagram of a universal host-powered multimedia speaker system 50 that includes a differential input/differential output (DIDO) speaker driver system 52a and 52b for each of an exemplary pair of wideband speakers 54a and 54b. It will be appreciated that host-powered speaker system 50 could alternatively be employed in a three-piece speaker system, analogous to speaker system 10 and including a sub-woofer speaker, or in a speaker system with more speakers. In one implementation, a DIDO amplification system 52 would be associated with and drive each speaker.

- [0022]** In the illustration of Fig. 2, DIDO speaker driver systems 52a and 52b have the same components, which are designated by common reference numerals and the suffices "a" and "b." While directed primarily to DIDO speaker driver system 52a and speaker 54a, the following description is similarly applicable to DIDO speaker driver system 52b and speaker 54b.
- [0023]** Host-powered speaker system 50 receives an audio signal at an audio input 56 from a multimedia audio circuit (e.g., a "sound card") of a multimedia computer, such as a personal computer (not shown). The audio signal is typically one channel of a multi-channel (e.g., stereo) audio signal that includes at least separate right and left audio channels.
- [0024]** In the illustrated stereo implementation, audio input 56 includes a left channel audio voltage input 58a and a right channel audio voltage input 58b that are directed to respective DIDO speaker driver systems 52a and 52b. Audio input 56 also includes an audio input ground 60. Audio input 56 may be connected to a multimedia audio circuit in any conventional manner, such as with a single prong "mini-jack" plug in which inputs 58a, 58b, and 60 are positioned coaxially with each other, or with any other audio system connecting device.
- [0025]** DIDO speaker driver system 52a includes an equalizer 68a and a pair of amplifiers 70a and 72a that are connected together in a differential input-differential output configuration. Equalizer 68a may be of a conventional type. Amplifier 70a receives the audio signal at a positive input terminal 74a, and amplifier 72a has its negative input terminal 76a connected to audio input ground 60. Amplifiers 70a and 72a are coupled through a capacitor 78a connected at their respective negative and positive input terminals 80a and 82a and so will amplify only the difference between the signal source and the signal ground. As

09877689 000001

known in the art, it should be recognized that the circuit represented here is not the only way to build a differential input amplifier.

[0026] Outputs 84a and 86a of amplifiers 70a and 72a are connected to speaker 54a at its positive terminal 90a and negative terminal 92a, respectively. As known in the art, it should be recognized that the circuit represented here is not the only way to build a differential output amplifier. DIDO speaker driver system 52a would typically include a volume-control circuit element, as known in the art, such as a ground-connected potentiometer (not shown) at audio input 58a.

[0027] Host-powered speaker system 50 further includes a host DC voltage input 96 and a host DC ground input 98 that are connected to available DC voltage and DC ground output connections on the multimedia host computer from which the audio signal is received. In one implementation, for example, host DC voltage input 96 and a host DC ground input 98 that are connected to available DC voltage and DC ground output connections of conventional audio or sound subsystems (e.g., "sound cards"), a dedicated host power output jack, or power available from a standard data port on the host such as a USB jack. Amplifiers 70a and 72a each include DC power and DC ground terminals that are connected to the corresponding host DC voltage input 96 and host DC ground input 98.

[0028] Host DC voltage input 96 and a host DC ground input 98 allow speaker system 50 to be powered by its hosting multimedia computer system, thereby eliminating the requirement for a relatively expensive stand-alone power supply. As a result, host-powered speaker system 50 can be used universally with multimedia computer systems in different countries having different electric power conventions, thereby eliminating a separate speaker system power supply that is specifically adapted for each electric power convention.

09877689 000001
000001 09877689

[illegible][illegible][illegible][illegible]

52a are connected. For example, power output 104 may be implemented as a socket on host computer system 100 on an audio card or sound sub-system 106, or at another socket location (the latter illustrated) such as a dedicated power port or a conventional data port that supplies DC power such as a USB jack. Sound sub-system 106 includes an audio signal source 108, designated V Signal, which generates an audio signal that is delivered at an audio output 110 with an audio ground output 111 for respective signal input 58a and input ground line 60 of DIDO speaker driver system 52a.

[0033] Host computer system 100 is modeled as including a power supply noise source 112, designated V Power Noise, that represents electrical noise that is carried on power supply 102. Power supply noise 112 may correspond to any or all of the noise voltage on the positive power rail or bus generated from the current draw of the digital, analog, and mechanical parts in host computer system 100 being dropped across the finite output impedance of power supply 102, the finite impedance of host computer system 100 power wiring, and noise generated from power supply 102 itself.

[0034] Sound sub-system 106 is modeled as including a ground noise source 114, designated V Gnd Noise, that represents the electrical noise difference between the sound sub-system ground 116 and the DC power ground 118 in host computer system 100. Ground noise 114 is generated from the current of the digital, analog, and mechanical parts in host computer system 100 flowing through the finite ground impedances 120 of the system.

[0035] The non-inverting and inverting amplification provided by respective amplifiers 70a and 72a, together with the coupled inputs 80a and 82a, cause amplifiers 70a and 72a operate in a differential input manner. Differential input amplification amplifies the difference between the

[illegible][illegible]

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

exemplary pair of headphone speakers 154a and 154b. Speakers 154a and 154b are commonly contained within separate earpiece housings (not shown) that may be joined by a headband 156, or a neckband, wires, etc. to form a headphone 158. DIDO speaker driver systems 52a and 52b are redrawn relative to the illustration of Fig. 2 to show with better clarity the connection to headphone 158. The description of DIDO speaker driver systems 52a and 52b referencing Fig. 2 is similarly applicable to audio amplification system 150.

[0039] As with multimedia speaker system 50, positive output 84a of amplifier 70a is connected to speaker 154a at its positive terminal 190a. However, negative output 86a of amplifier 72a is connected through a resistor 94a to negative terminals 192a and 192b of respective speakers 154a and 154b. Speakers 154a and 154b may be connected to DIDO speaker driver system 52a and 52b in any conventional manner, such as with a single prong “mini-jack” plug in which couplings that are positioned coaxially with each other, or with any other audio system connecting device.

[0040] Resistors 194a and 194b function to sum together the voltages on outputs 86a and 86b of amplifiers 72a and 72b, which sum is applied to speakers 154a and 154b as a common “ground.” In one implementation, resistors 194a and 194b have the same impedance as speakers 154a and 154b, so that a natural virtual ground or virtual voltage common is established at terminals 192a and 192b to prevent current from flowing between DIDO speaker driver systems 52a and 52b. Thus, full normal stereo operation can be provided with the noise rejection properties and no risk of damage to the amplifiers.

[0041] It will be appreciated that alternative implementations could employ other impedances for resistors 194a and 194b. For example, different impedance values for resistors 194a and 194b can provide an

“enhanced stereo” feature, which is illustrated by way of an exaggerated example. If resistors 194a and 194b have the same very high impedance relative to speakers 154a and 154b, current will no longer flow to inverting amplifier outputs 86a and 86b from speaker terminals 192a and 192b. Instead, current will flow between non-inverting amplifier outputs 90a and 90b.

[0042] As a result, left speaker 154a will play an audio signal represented as L-R corresponding to the difference between the left and right audio channels, and right speaker 154b will play an audio signal represented as R-L corresponding to the difference between the right and left audio channels. As a result, all common mode (i.e., “mono”) audio signal will be canceled out. In less exaggerated implementations, the common mode audio signal can be partly cancelled, thereby resulting in a relative enhancement of stereo mode audio, with corresponding improvement in the perceived audio characteristics.

[0043] Fig. 5 is a schematic diagram of a switch mechanism 200 for switching audio amplification system 150 (or 50) between couplings to multimedia speakers 54 or to a headphone 158. For example, switch mechanism 200 may be operated by action of connecting an electrical jack (not shown) for headphone 158 into a corresponding plug that is coupled to amplification system 150 and incorporated into a multimedia speaker system. Alternatively, switch mechanism may be controlled in any alternative manner, as is known in the art.

[0044] Switch 200 is illustrated by solid lines as being in a multimedia speaker connection position for coupling audio amplification system 150 (or 50) to multimedia speakers 54, which are indicated the reference numerals for the corresponding speaker terminals. Dashed lines indicate an alternative headphone connection position by which switch mechanism 200 may couple audio amplification system 150 (or 50) to

speakers 154 of headphone 158, as indicated the reference numerals for the corresponding speaker terminals. As indicated, switch mechanism 200 accommodates the connections between resistors 194 in the headphone connection position.

[0045] Many conventional power amplifier integrated circuits offer signal ground pins that are separated from the integrated power ground pins. DIDO speaker driver system 52a may be implemented with any type of amplifier integrated circuit that provides access, or pseudo access, to both inverting and non-inverting inputs of differential gain stages, including standard “ground sensing” operational amplifier integrated circuits and many standard audio amplifier integrated circuits. The differential read or input of the signal voltage offers very wide band attenuation of the common-mode noise even when equalization circuits cause impedance imbalances on the input pins.

[0046] Moreover, it will be appreciated that that many differential drive or bridge-tied load (BTL) configurations could provide the differential output arrangement of the present invention. Some dual power amplifier integrated circuits have gain setting configurations that allow the inverting input of one amplifier to be tied to the non-inverting input of the other amplifier for differential output drive. Other configurations simply voltage divide the output of the non-inverting amplifier and feed this signal into the inverting input of the second amplifier. Still other integrated circuits offer amplifier pairs as one inverting and the other non-inverting, and differential output is obtained by connecting two inputs to the same signal source.

[0047] Having described and illustrated the principles of our invention with reference to a preferred embodiment thereof, it will be apparent that the invention can be modified in arrangement and detail without departing from such principles. In view of the many possible embodiments to

which the principles may be put, it should be recognized that the detailed embodiment is illustrative only and should not be taken as limiting the scope of our invention. Accordingly, we claim as our invention all such modifications as may come within the scope and spirit of the following claims and equivalents thereto.

09877689-00004